Docket No. TRANSMITTAL OF FORMAL DRAWINGS G0186 In Re Application Of: APR 1 8 2002 Serial No. g Date Batch No. Examiner 10/053,256 01/18/2002 2185 TWO-STEP SOURCE SIDE IMPLANT FOR IMPROVING SOURCE RESISTANCE AND SHORT CHANNEL EFFECT IN DEEP SUB-0.18um FLASH MEMORY TECHNOLOGY Invention: Address to: RECEIVED **Assistant Commissioner for Patents** Washington, D.C. 20231 APR 2 3 2002 Technology Center 2100 Transmitted herewith are: Six (6) sheets of formal drawing(s) for this application. X Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c). Dated: 4/10/02 H. Donald Nelson, Reg. No. 28,980 I certify that this document and attached formal drawings are being deposited on 4/10 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents,

Signature of Person Mailing Correspondence

H. Donald Nelson

Typed or Printed Name of Person Mailing Correspondence

Washington, D.C. 20231.